

FIG. 1

102

FIG. 1 is a block diagram of a burst mode receiver system.

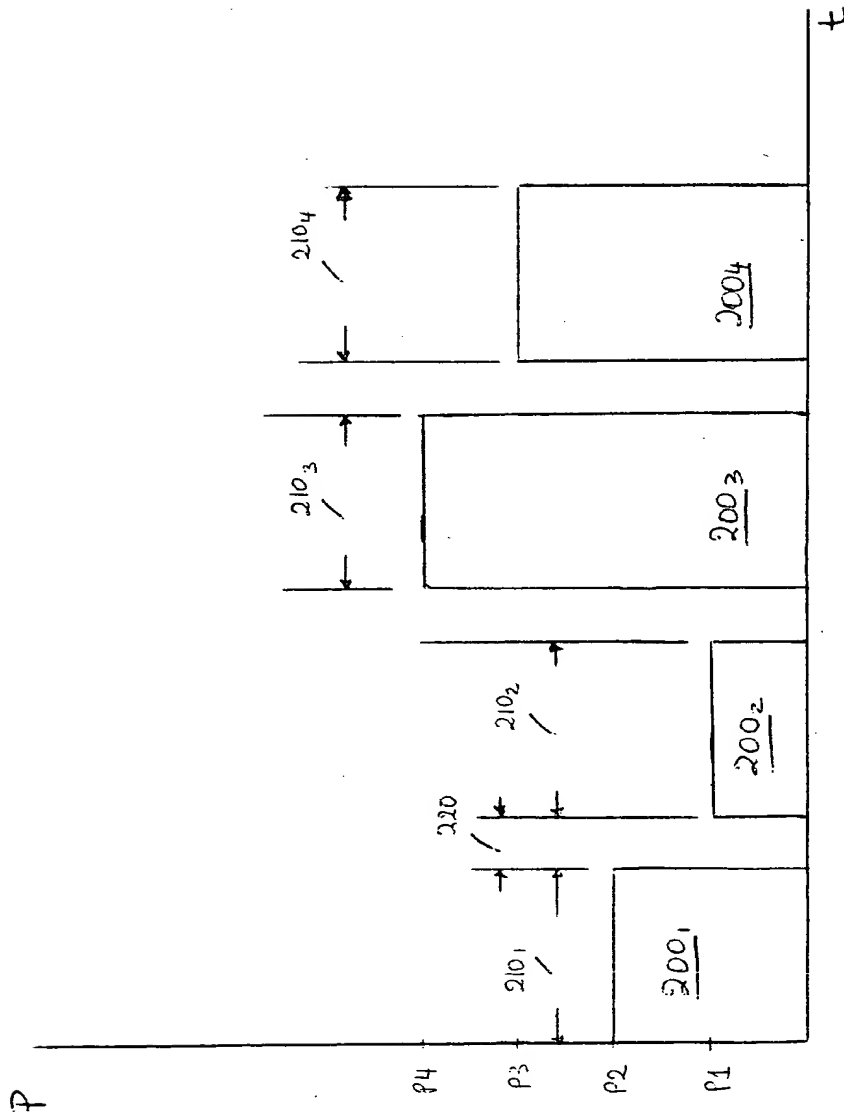


FIG. 2A

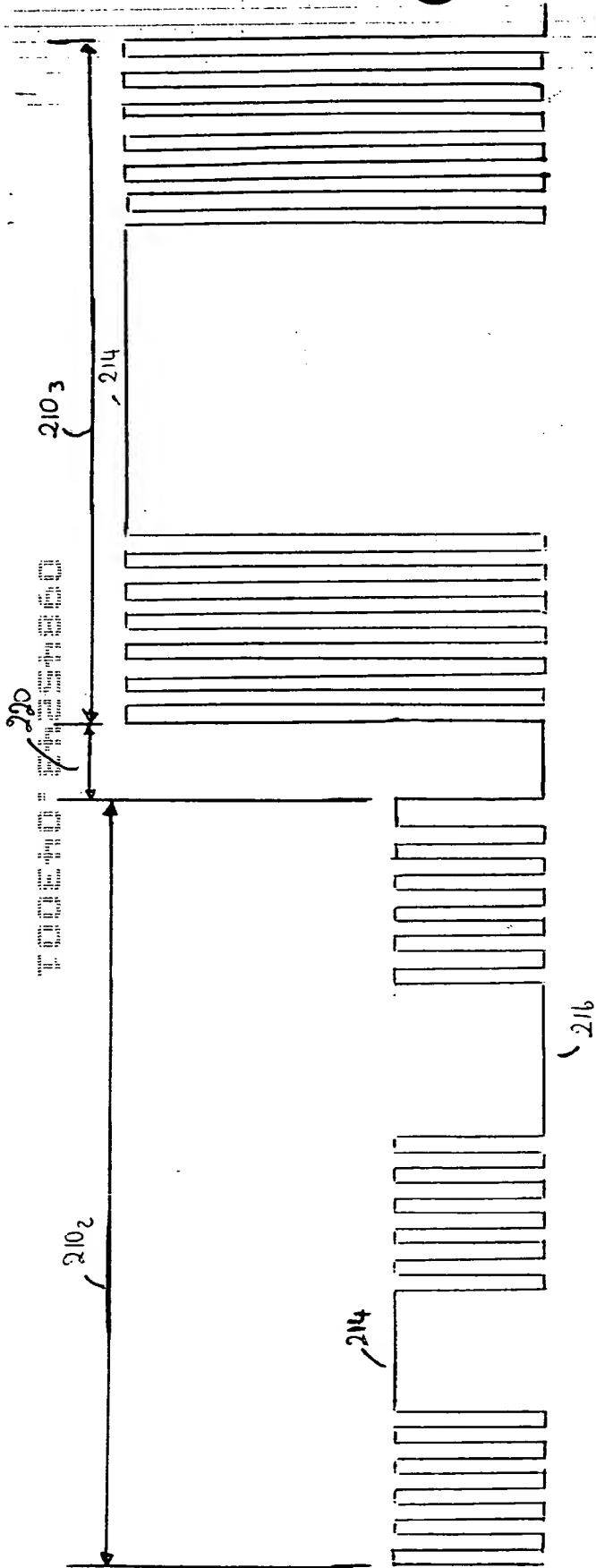


FIG. 2B

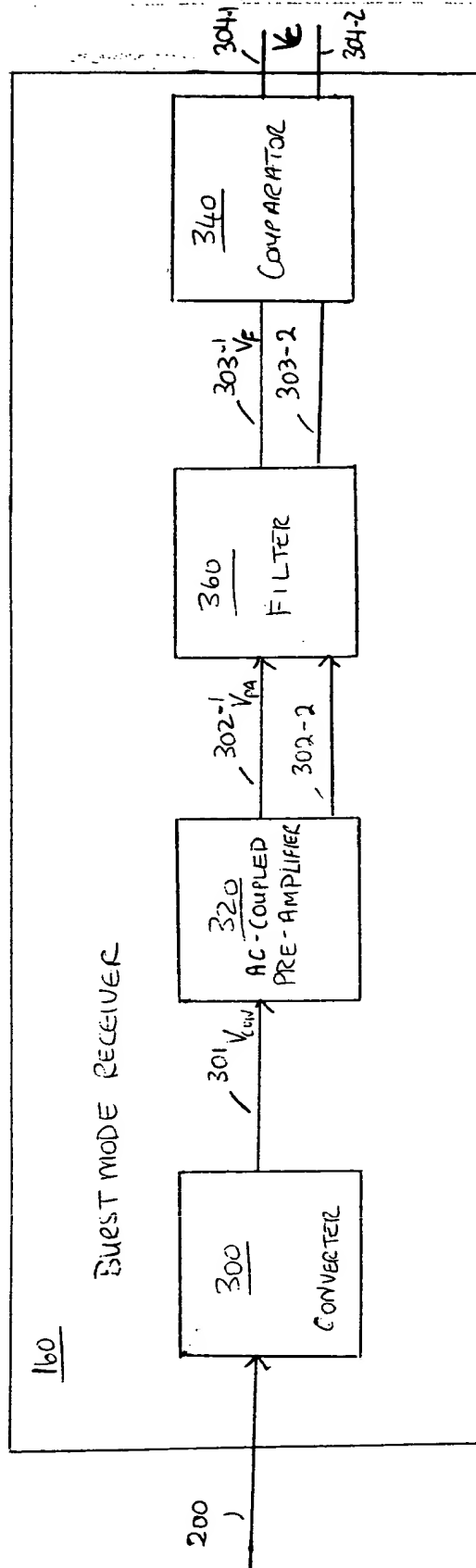


FIG. 3

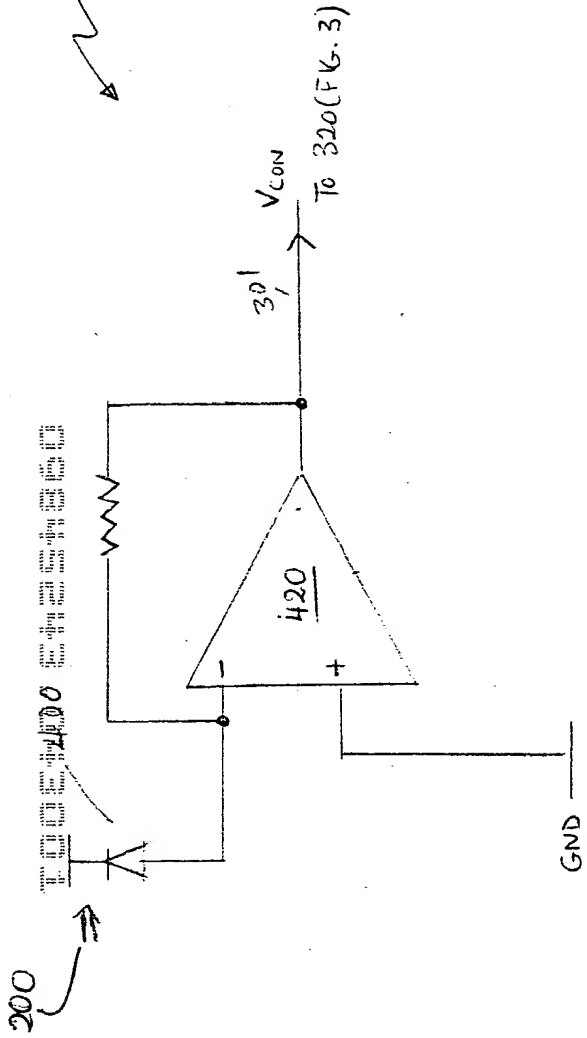


FIG. 4

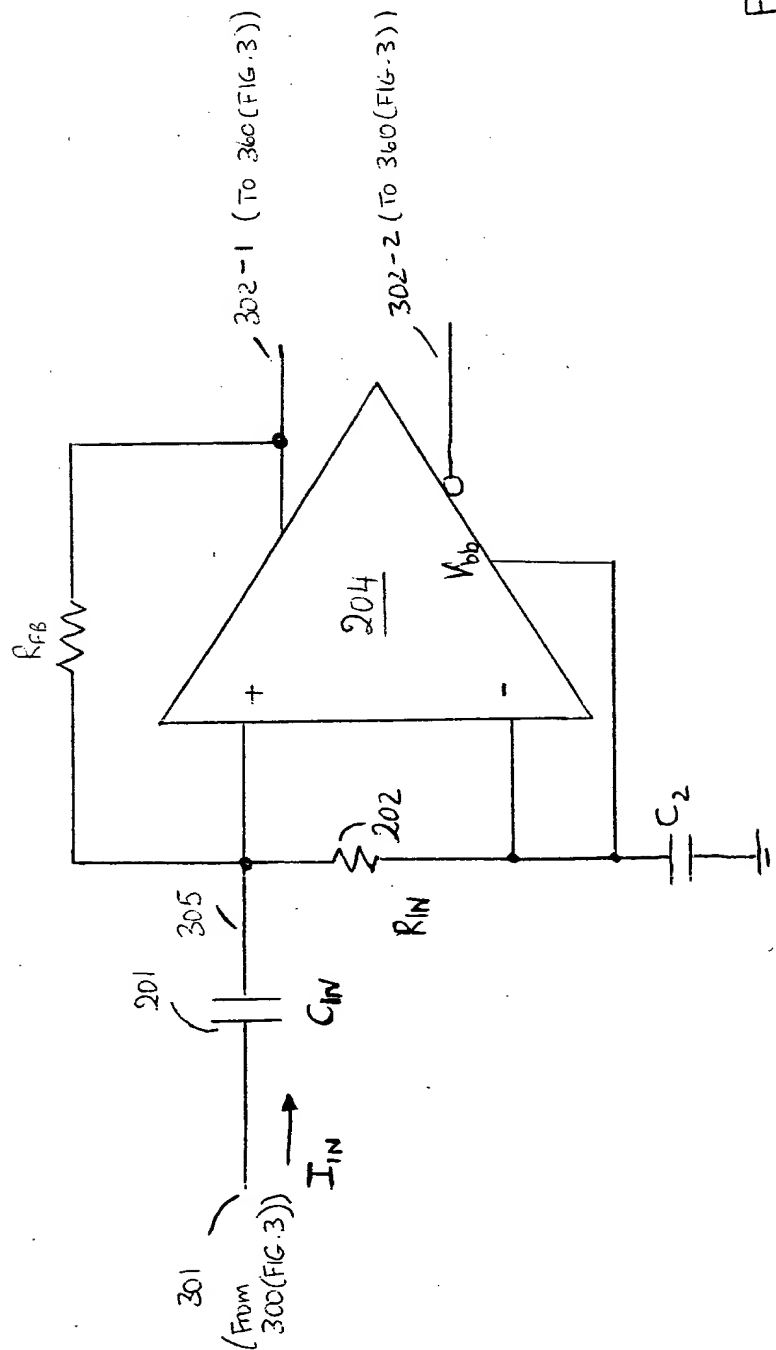


FIG. 5A

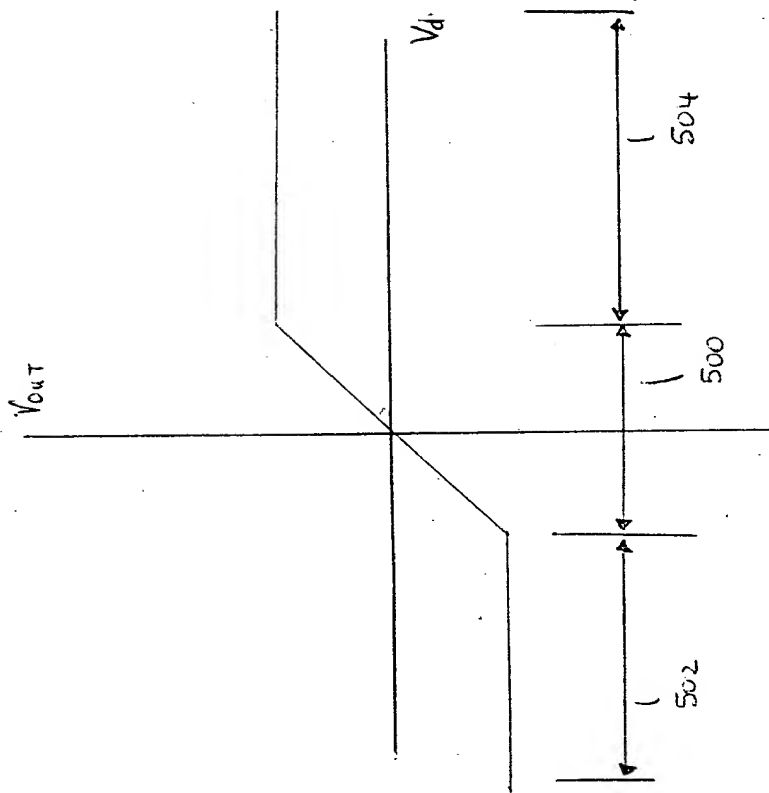
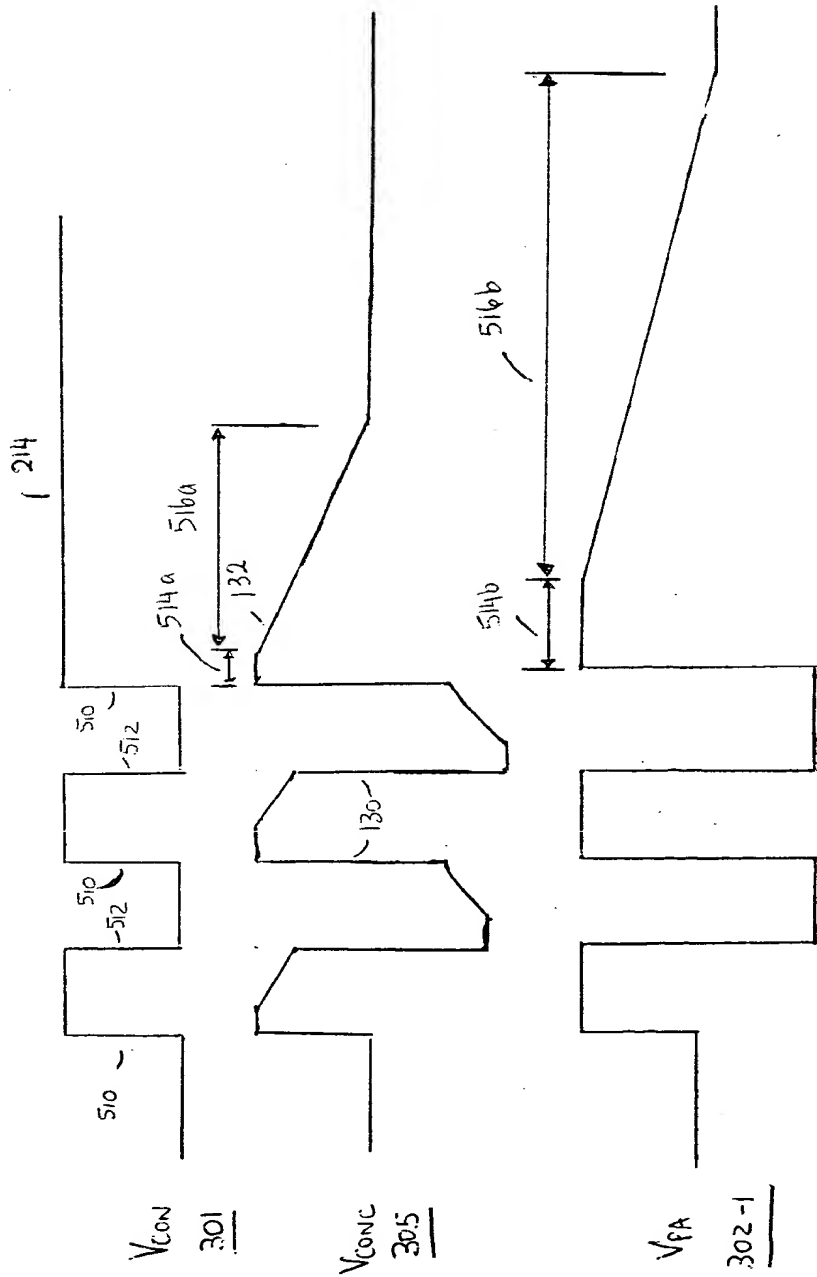


FIG. 5B

FIG. 5C





20

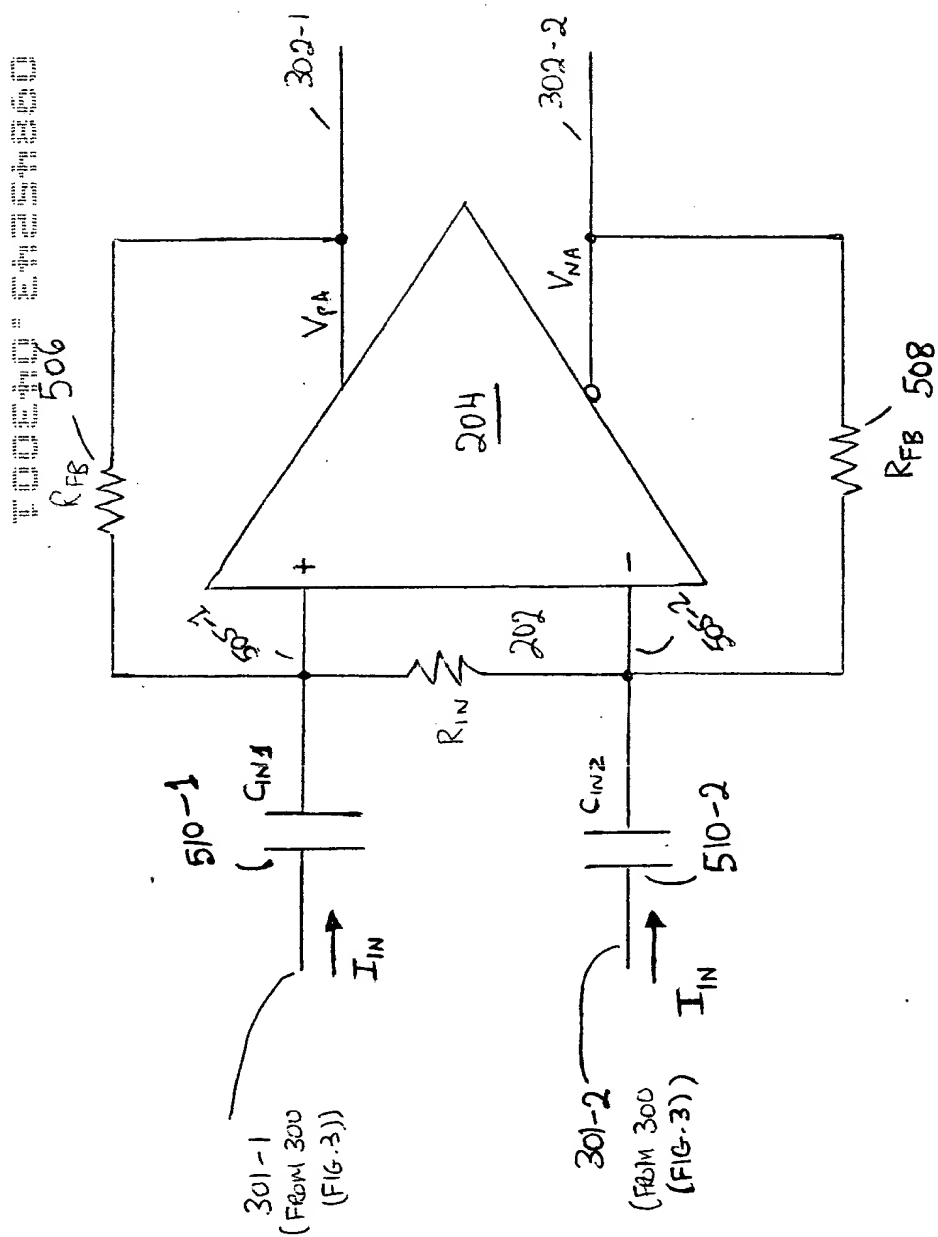


FIG. 5D

FIG. 6A

340

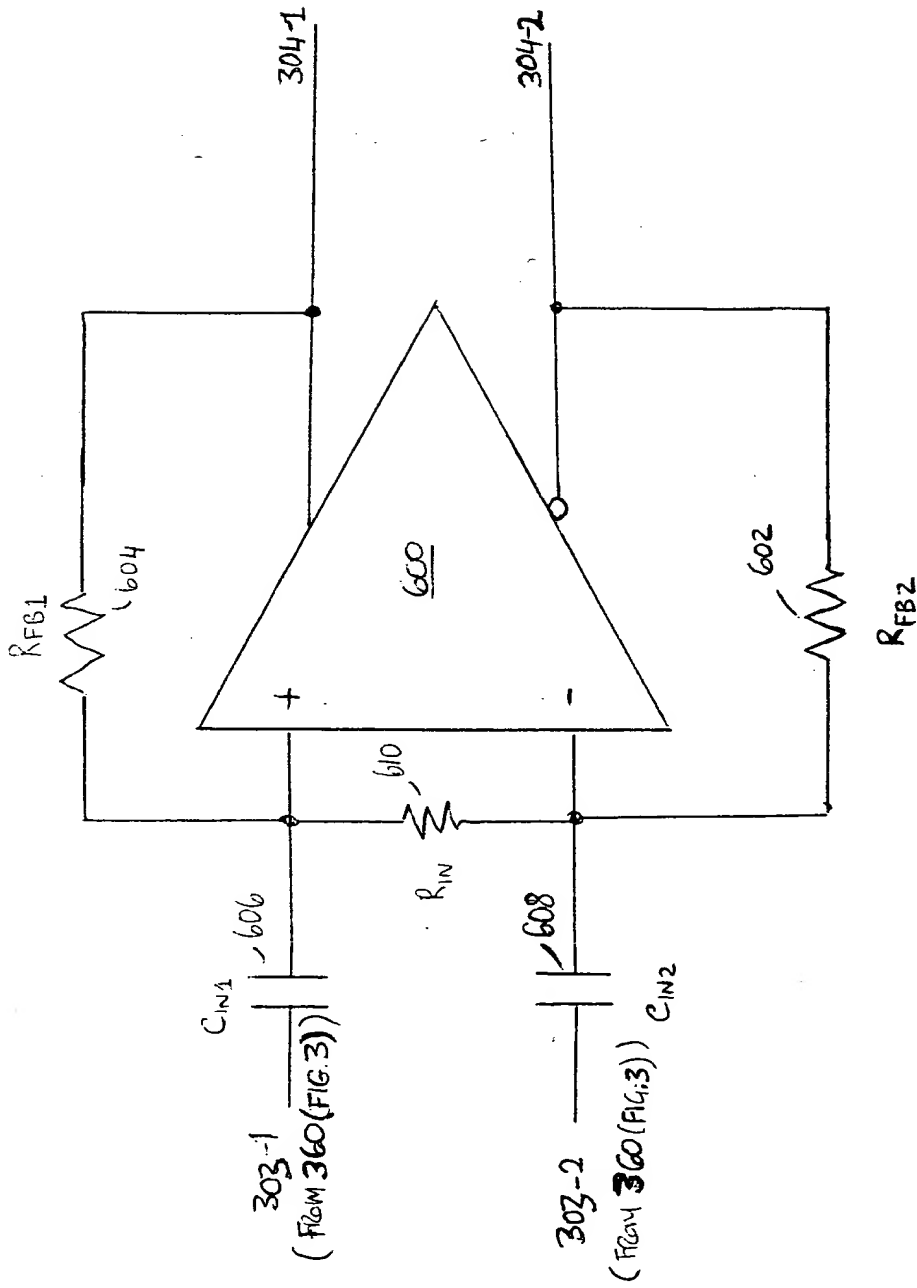


FIG. 6A

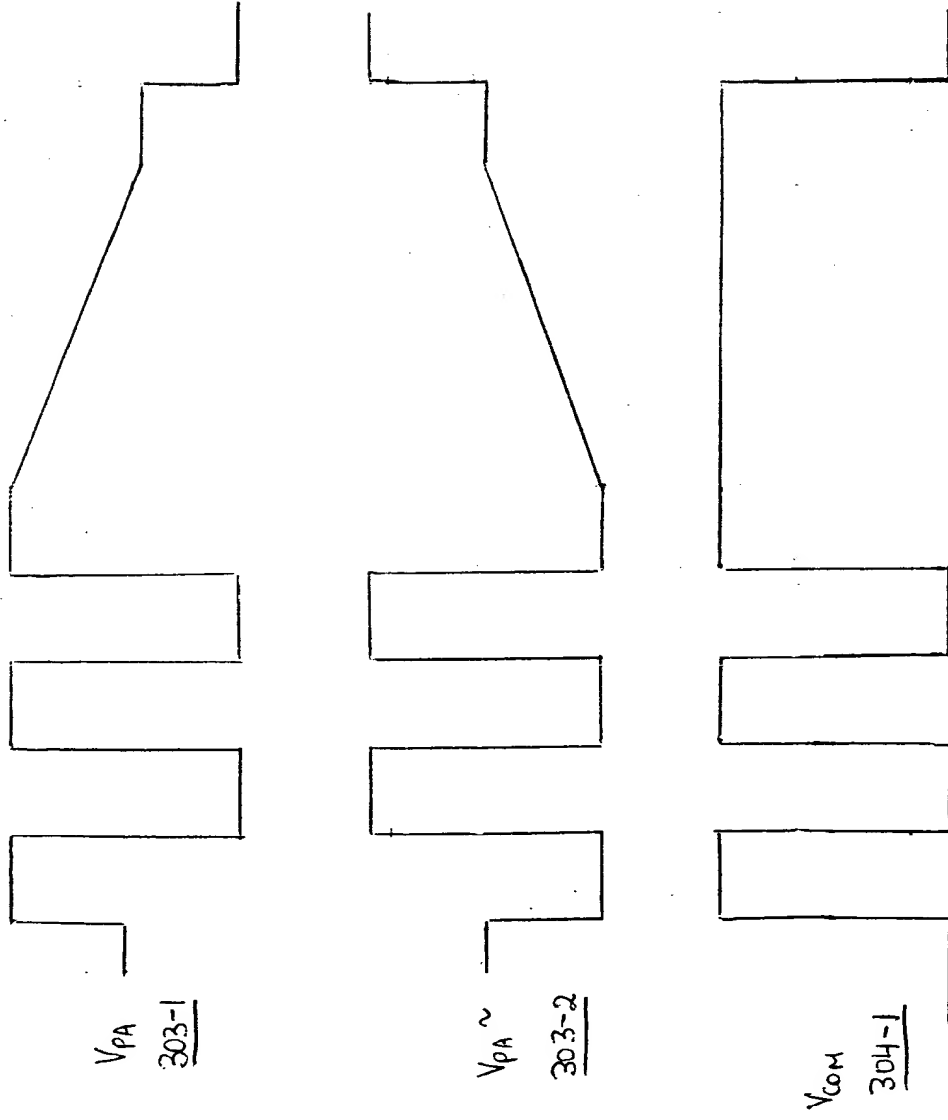


FIG. 6B

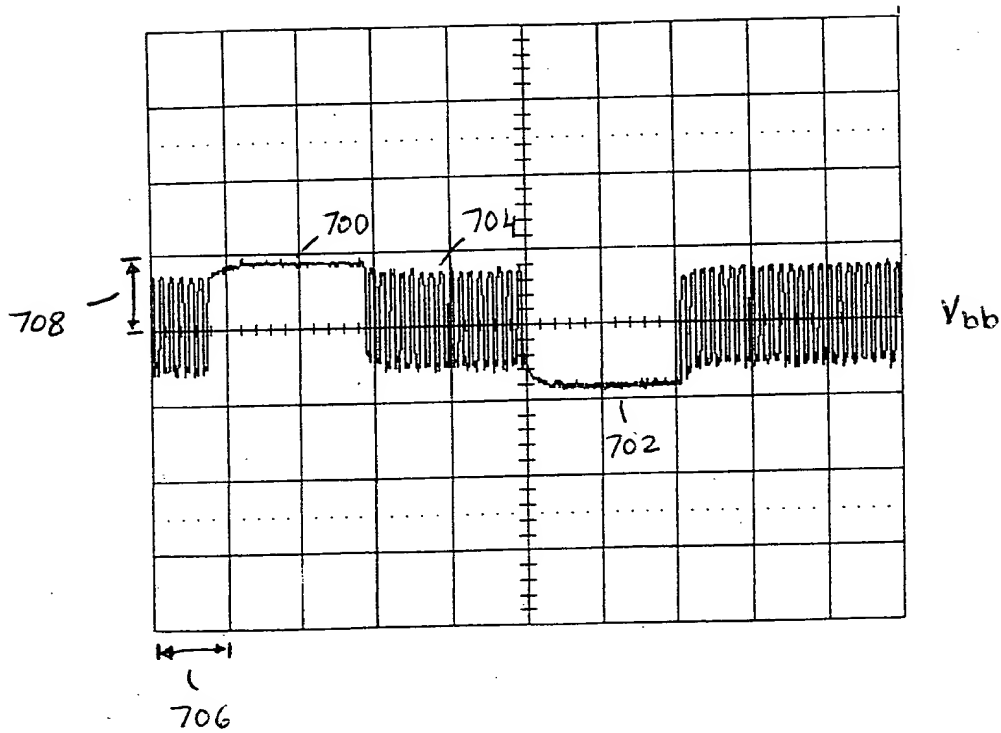


FIG. 7

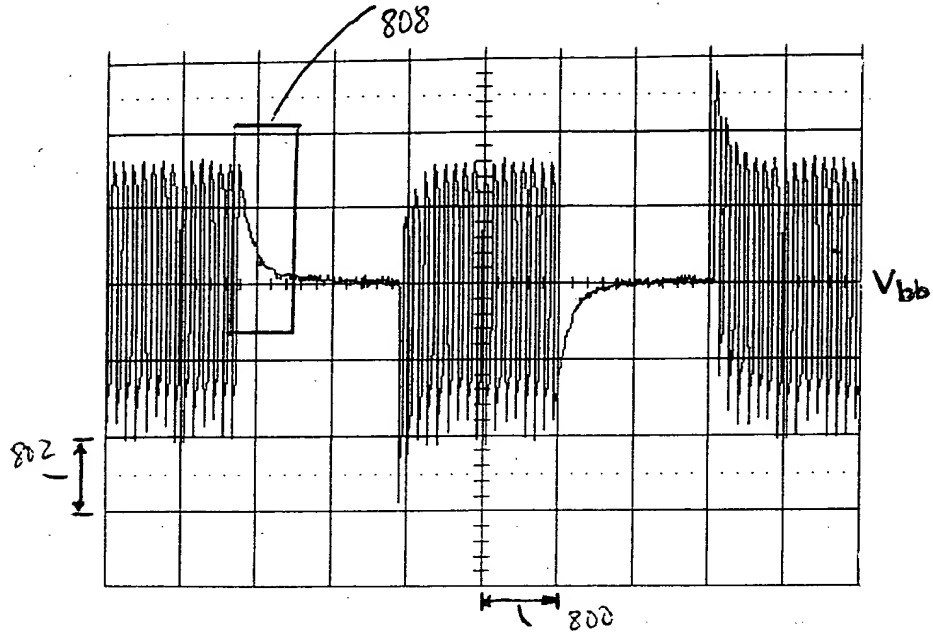


FIG. 8A

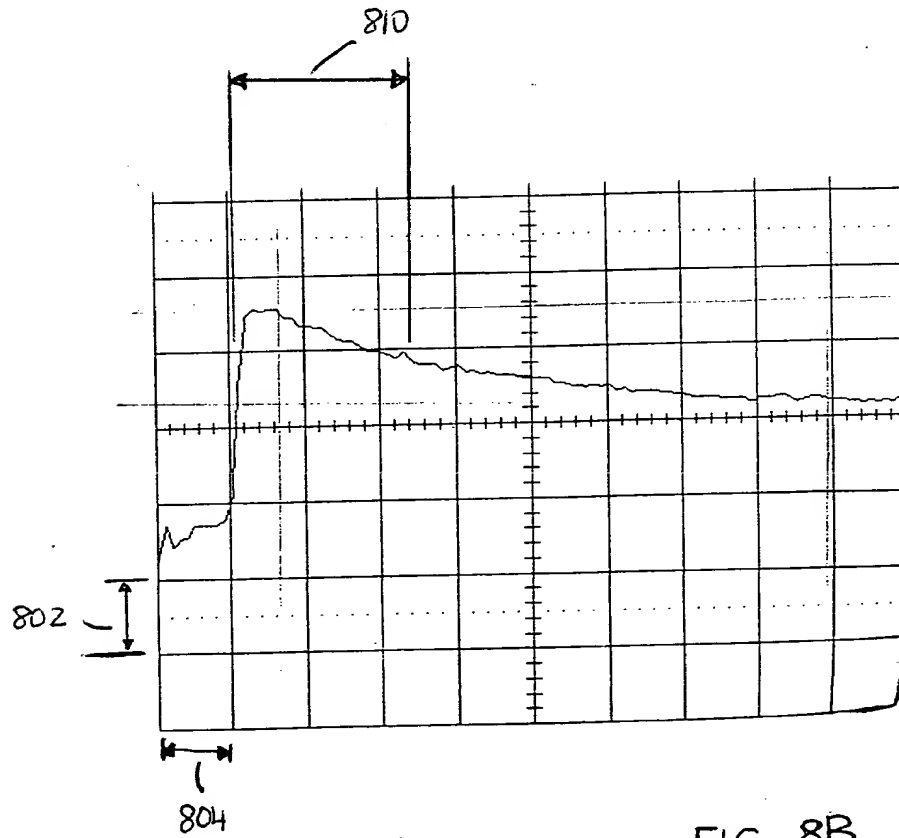


FIG. 8B

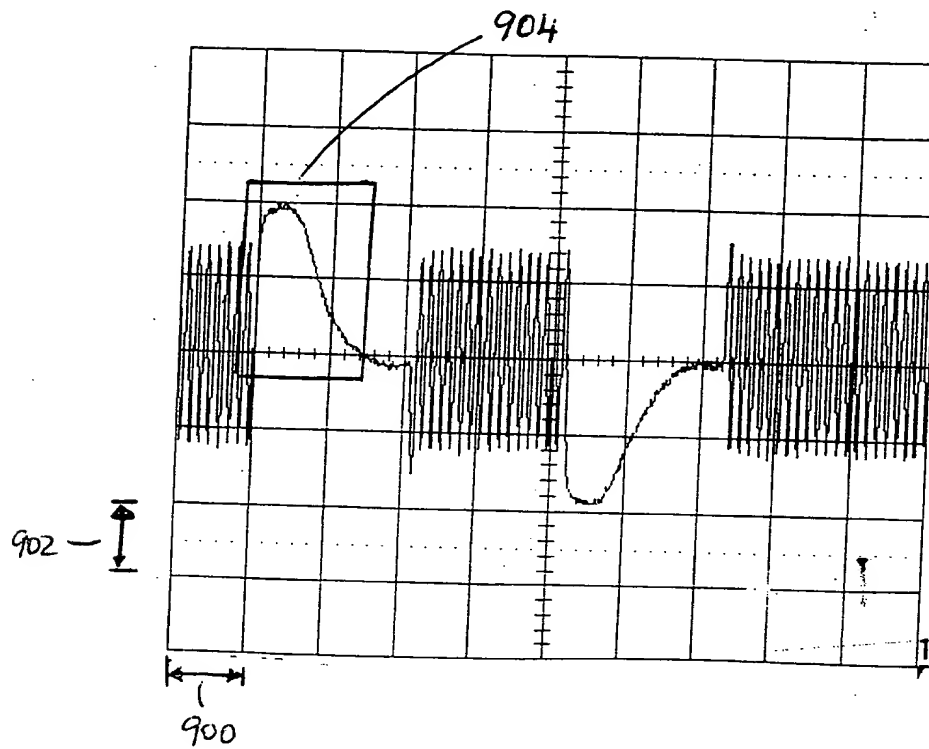


FIG. 9A

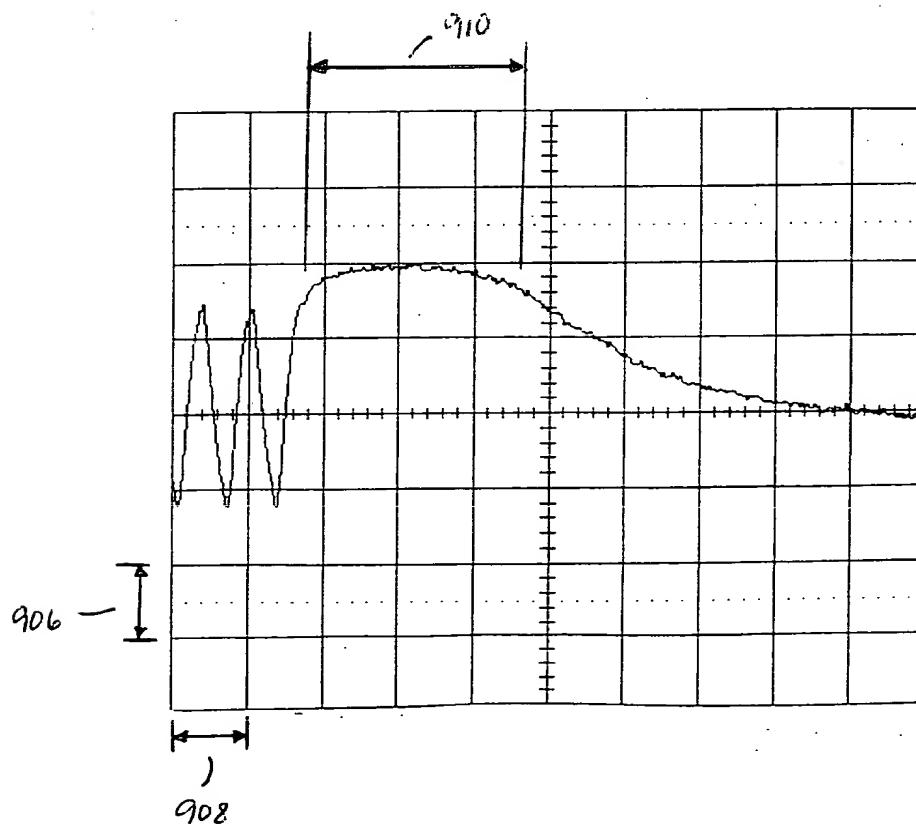


FIG. 9B

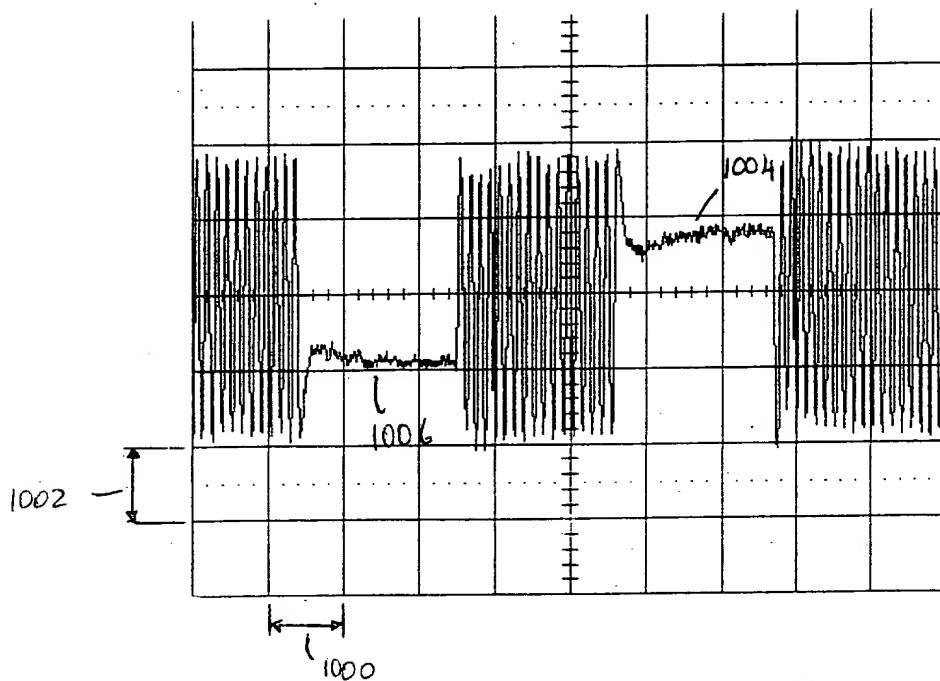


FIG. 10A

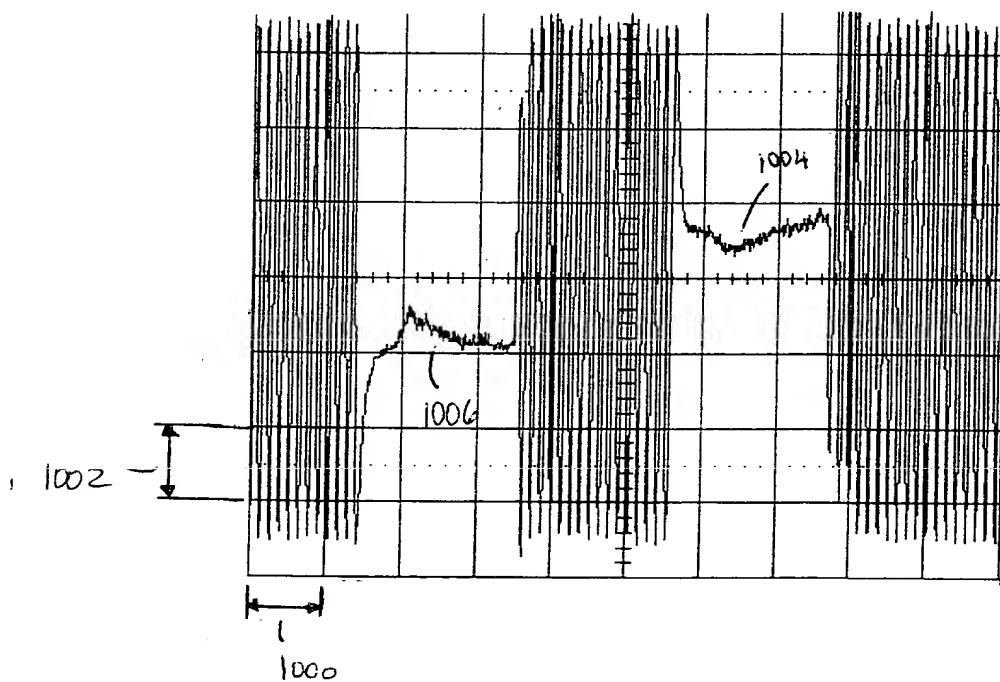


FIG. 10B

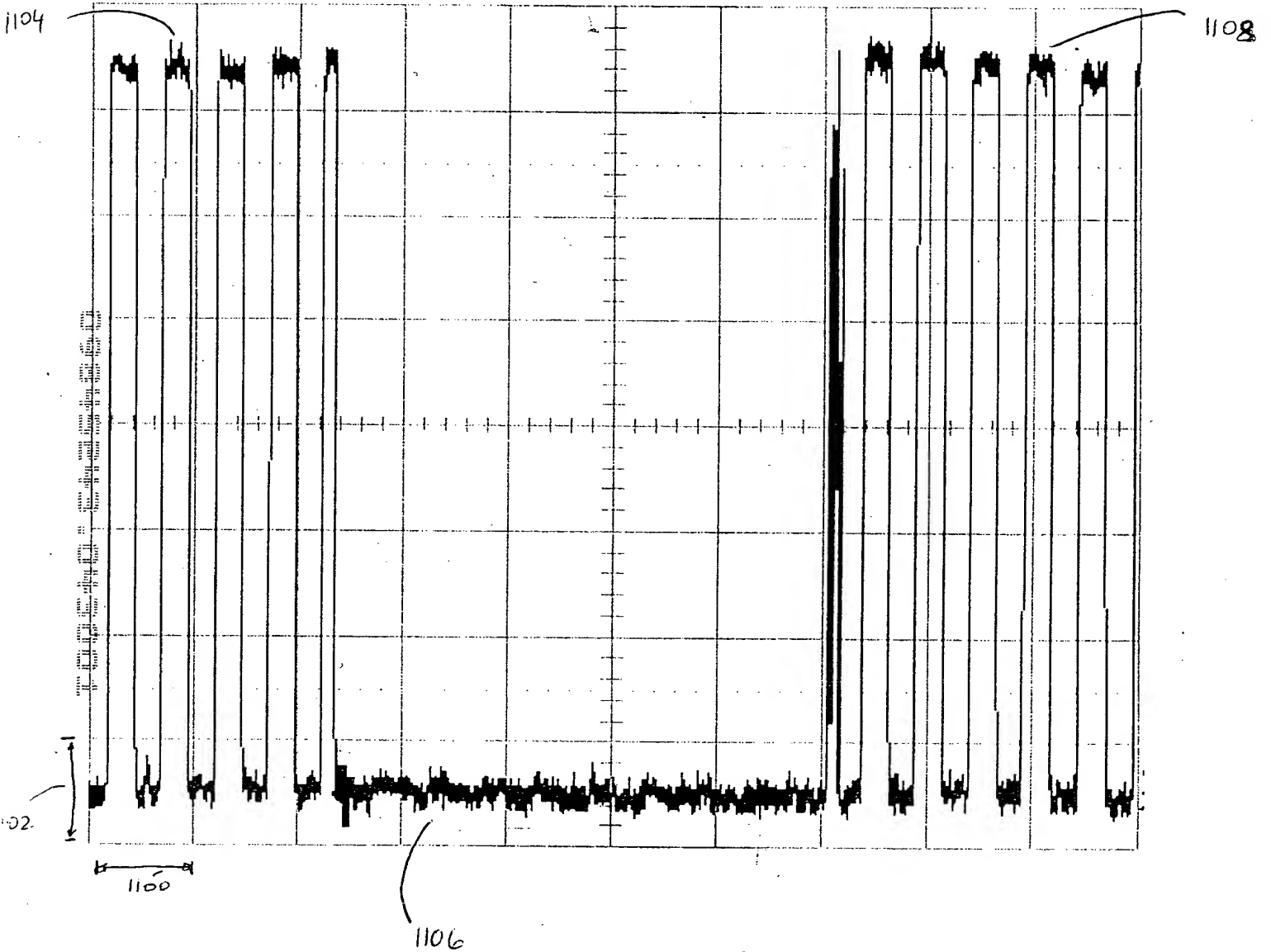


FIG. 11 A



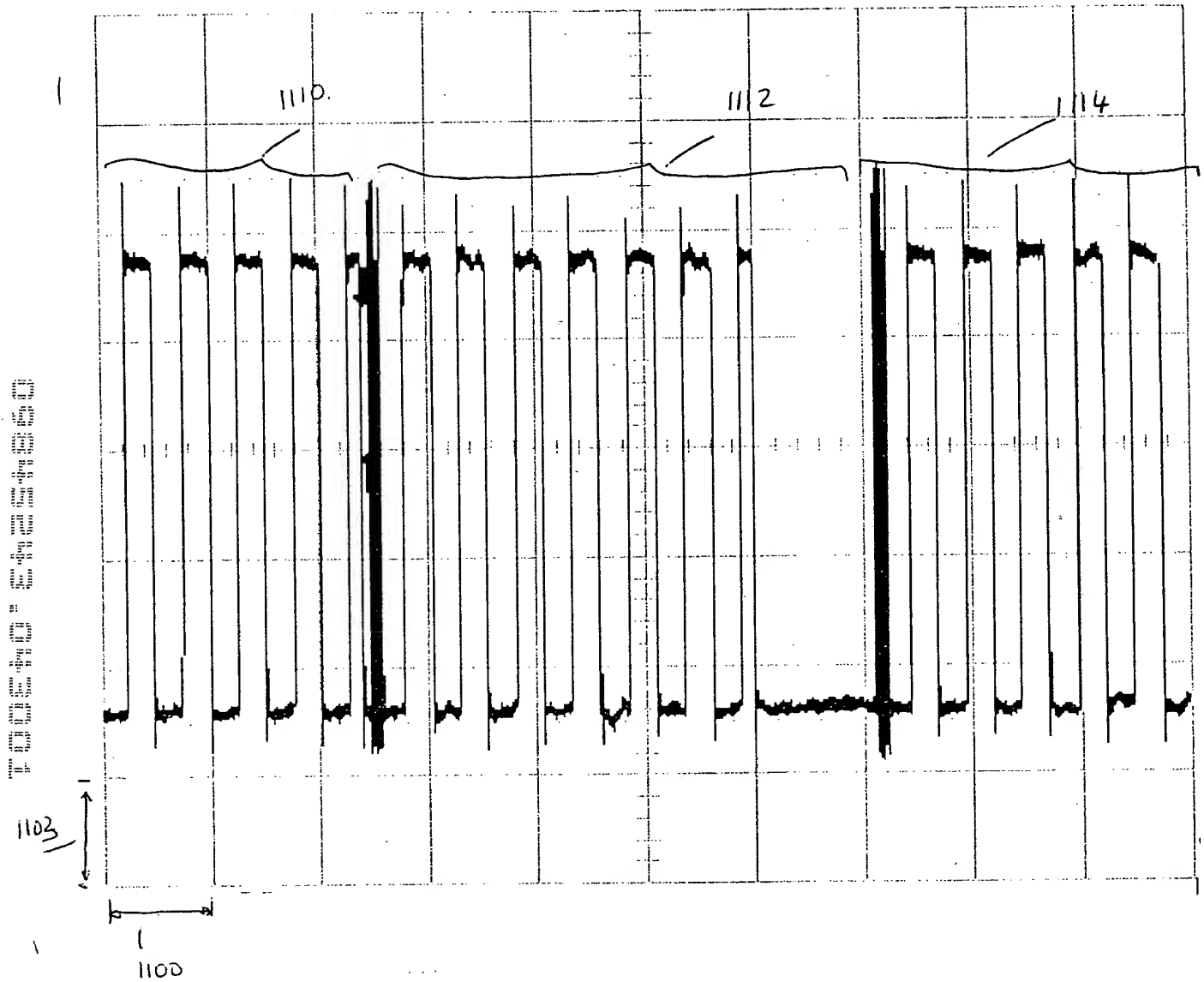


FIG. 11 B

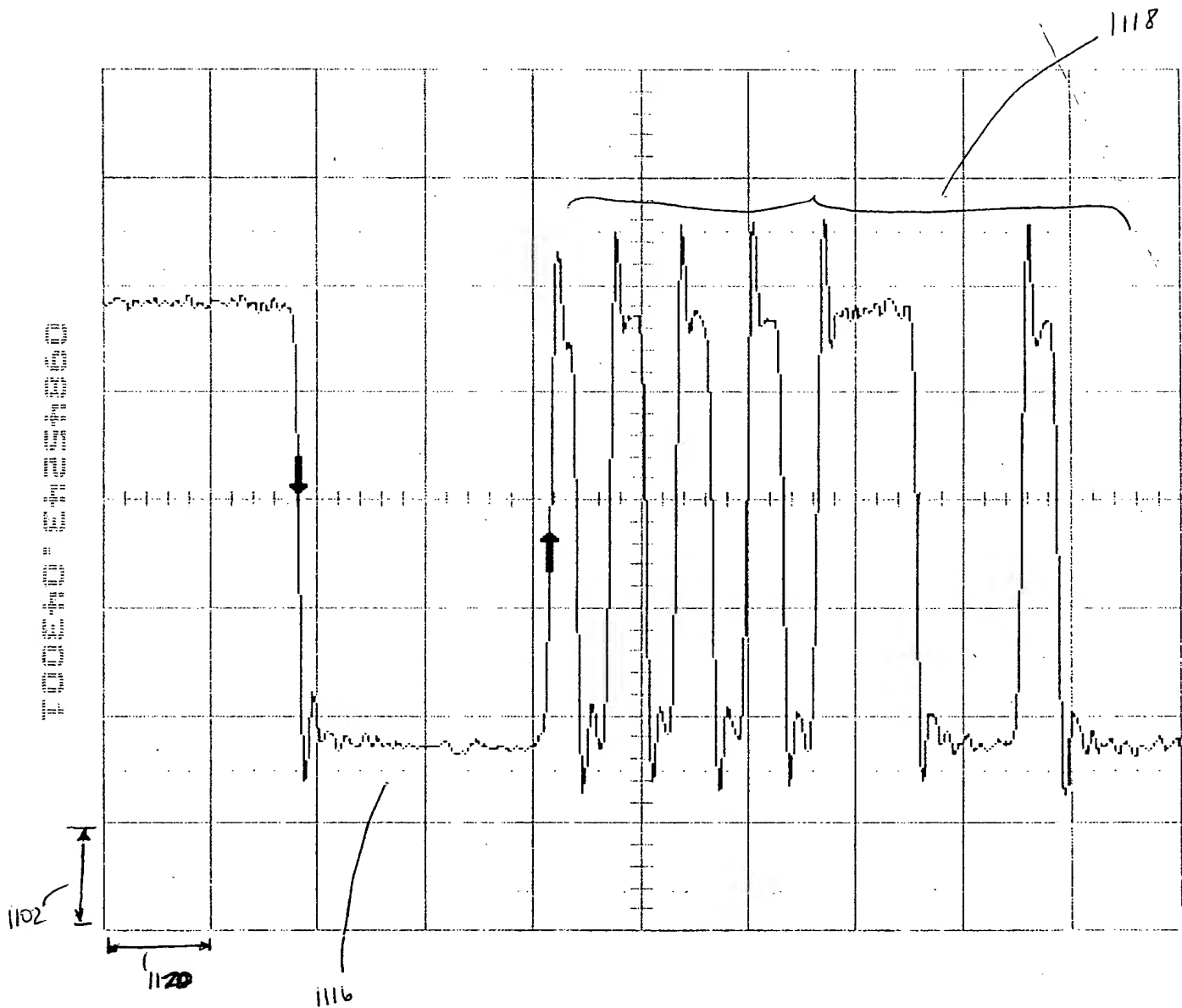


FIG. 11 C